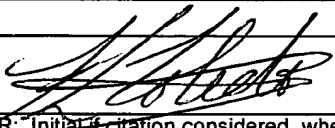


<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>				Docket Number		Application Number	
				M4065.0127/P127-A		Not Yet Assigned	
				Applicant(s)			
				Kie Y. Ahn et al.			
				Filing Date		Group Art Unit	
September 12, 2000		2815					
<b>U.S. PATENT DOCUMENTS</b>							
*EXAMINER INITIAL	REF	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
FL7	A	5,770,476	6/23/98	Stone	438	106	5/15/97
FL7	B	5,530,288	6/25/96	Stone	257	700	10/12/94
FL7	C	5,539,241	7/23/96	Abidi et al.	257	531	2/15/95
FL7	D	5,564,617	10/15/96	Degani et al.	228	6.2	6/7/95
FL7	E	5,674,785	10/7/97	Akram et al.	437	217	11/27/95
FL7	F	5,688,711	11/18/97	Person et al.	437	60	5/10/96
<b>FOREIGN PATENT DOCUMENTS</b>							
	REF	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translations YES NO
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
FL7	G	Burns; "Applications for GaAs and Silicon Integrated Circuits in Next Generation Wireless Communication System"; IEEE Journal of Solid-State Circuits, Vol. 30, No. 10, October 1995; pg. 1088-1095.					
FL7	H	Van Tuyl et al.; "A Manufacturing Process for Analog and Digital Gallium Arsenide Integrated Circuits"; IEEE Transactions on Microwave Theory and Techniques, Vol. MTT-30, No. 7, July 1982; pg. 935-942.					
FL7	I	Nguyen et al.; "Si IC-Compatible Inductors and LC Passive Filters"; IEEE Journal of Solid-State Circuits, Vol. 25, No. 4, August 1990; pg. 1028-1031.					
FL7	J	Chang et al.; "Large Suspended Inductors on Silicon and Their Use in a 2-μm CMOS RF Amplifier"; IEEE Electron Devices Letters, Vol. 14, No. 5, May 1993; pg. 246-248.					
FL7	K	Burghartz et al.; "Multilevel-Spiral Inductors Using VLSI Interconnect Technology"; IEEE Electron Devices Letters, Vol. 17, No. 9, September 1996; pg. 428-430.					
FL7	L	Burghartz et al.; "Integrated RF and Microwave Components in BiCMOS Technology"; IEEE Transactions on Electron Devices, Vol. 43, No. 9, September 1996; pg. 1559-1570.					
FL7	M	Craninckx et al.; "A 1.8-GHz Low-Phase-Noise Spiral-LC CMOS VCO"; IEEE Symposium on VLSI Circuits Digest of Technical Papers, 1996; pg. 30-31.					
FL7	N	Pieters et al.; "Spiral Inductors Integrated in MCM-D using the Design Space Concept"; IEEE International Conference on Multichip Modules and High Density Packaging, 1998; pg. 478-483.					
FL7	O	Samber et al.; "Low-Complexity MCM-D Technology with Integrated Passives for High Frequency Applications"; IEEE International Conference on Multichip Modules and High Density Packaging, 1998; pg. 285-290.					
FL7	P	Hartung; "Integrated Passive Components in MCM-Si Technology and their Applications in RF-Systems"; IEEE International Conference on Multichip Modules and High Density Packaging, 1998; pg. 256-261.					
FL7	Q	Hitko et al.; "A 1V, 5mW, 1.8GHz, Balanced Voltage-Controlled Oscillator with an Integrated Resonator"; Proc. Symp. on Low Power Electronics and Design, Monterey CA, 1997; pg. 46-51.					
FL7	R	Ahrens et al.; "A 1.4-GHz 3-mW CMOS LC Low Phase Noise VCO Using Tapped Bond Wire Inductances"; Proc. Symp. on Low Power Electronics and Design, Monterey CA, 1998; pg. 16-19.					
FL7	S	Saia et al.; "Thin Film Passive Elements on Polyimide Film"; IEEE International Conference on Multichip Modules and High Density Packaging, 1998; pg. 349-353.					

EXAMINER	DATE CONSIDERED
	7/25/03
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.	

